



SAK-TC277TP-64F200N DC

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Parametrics

Parametrics	SAK-TC277TP-64F200N DC
A/D Input Lines	60
CAN Nodes	4
Clock Frequency max	200.0 MHz
DMA Channels	64
DSP Functionality	yes
EEPROM Emulation	yes

Parametrics	SAK-TC277TP-64F200N DC
External Bus Interface	no
Fast Flash Programming	no
FlexRay™	yes
Floating Point Unit	yes
I/O Operation Voltages	3.3 V 5.0 V
Instruction Set Architecture	TriCore™ v1.6P (32-bit) TriCore™ v1.6E (32-bit)
Instruction Width ([bits])	32/16
Number of ADC Modules	14.0
On-chip Clock Generation	yes
On-chip Voltage Regulator	yes
Oscillator Watchdog	yes
Program Memory	4.0 MByte
Real Time Clock	yes
SRAM (incl. Cache)	472.0 kByte

Parametrics	SAK-TC277TP-64F200N DC
Temperature	-40°C – +125°C
Touch/LED Matrix Control	no
Type of Memory	Flash
Watchdog Timer	yes

Documents

Application Notes

- [An introduction to MTV Multi-Core Debug Solution \(MCDS\) Trace ViewerEN](#)

01_00 | 2018-04-03 | pdf | 1.1 MB

- [EMC and System-ESD Design Guidelines for Board LayoutEN](#)

01_00 | 2018-03-15 | pdf | 3 MB

Material Content Sheet Info

- [MCDS 2018-01-22 19-25-32 MA001660236 PG-LFBGA-292-6EN](#)

01_00 | 2018-02-05 | pdf | 28 KB

Product Selection Guide

- [Power and Sensing Selection Guide 2018EN](#)

00_00 | 2018-02-19 | pdf | 23.9 MB

[Presentations](#)

- [Microcontroller Pocket GuideEN](#)

01_00 | 2018-02-22 | pdf | 2.6 MB

Order

Sales Product Name	SAK-TC277TP-64F200N DC
OPN	TC277TP64F200NDCKXUMA1
Product Status	active and preferred
Package name	PG-LFBGA-292
Completely lead free	yes
Halogen free	yes
RoHS compliant	yes
Packing Size	1000
Packing Type	TAPE & REEL
Moisture Level	
Moisture Packing	DRY